In the Claims

1. (Currently Amended) An apparatus comprising:

a first solid-state memory die;

a second solid-state memory die; and

a controller sensing one or more operating parameters for the first and the second solid-state memory die and making intelligent decisions on where to write data, based on the operating parameters, wherein the operating parameters comprise temperature, current draw, or access time.

2. (Original) The apparatus of claim 1 wherein the first and second solid-state memory die comprises flash memory, MRAM, SRAM, DRAM, FRAM, or polymer memory.

3. (Currently Amended) The apparatus of claim 1 wherein the operating parameters comprise temperature, current draw, [or] and access time.

4. (Original) The apparatus of claim 1 further comprising a database storing known operating models for the first and the second die.

5. (Original) The apparatus of claim 1 further comprising a database storing known operating models for the first and the second solid-state memory die, and wherein the controller senses one or more operating parameters for the first and the second solid-state memory die and accesses the known operating models for the first and the second die and makes intelligent decisions on where to write data, based on the operating parameters and the known operating models.

6. (Original) The apparatus of claim 1 further comprising a File Access Table (FAT) storing available memory locations within the first and the second die.

7. (Withdrawn)
8. (Withdrawn)
9. (Withdrawn)
10. (Withdrawn)
11. (Withdrawn)
12. (Withdrawn)
13. (Withdrawn)
14. (Withdrawn)
15. (Withdrawn)

2